




Form PTO-1449  <b>INFORMATION DISCLOSURE CITATION IN AN APPLICATION</b>  (Use several sheets if necessary)		Docket Number (Optional) <b>3351.8US (96-0223.07/US)</b>  Application Number <b>N t yet assign d</b>  Applicant <b>Br nt K th</b> <span style="float: right;"><b>2818</b></span>  Filing Date <b>July 14, 2003</b> Group Art Unit <b>Unkn wn</b>					
<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
W	5,940,321	08/1999	Takeuchi et al.				
W	5,949,698	09/1999	Shirley				
W	6,005,265	12/1999	Kuroda et al.				
W	6,018,172	01/2000	Hidaka et al.				
W	6,034,879	03/2000	Min et al.				
W	6,043,562	03/2000	Keeth				
W	6,084,307	07/2000	Keeth				
W	6,094,370	07/2000	Takashima				
W	6,185,123	02/2001	Allen et al.				
W	6,188,598	02/2001	Mueller et al.				
W	6,222,275	04/2001	Keeth				
W	6,243,311	06/2001	Keeth				
<b>FOREIGN PATENT DOCUMENTS</b>							
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
<b>OTHER DOCUMENTS</b> <span style="float: right; font-size: small;">(Including Author, Title, Date, Pertinent Pages, Etc.)</span>							
W		IBM Technical Disclosure Bulletin, Vol. 30, No. 3, August 1987, pps. 1314-1315.					
W		IBM Technical Disclosure Bulletin, Vol. 30, No. 11, April 1988, pps. 246-248.					
EXAMINER <b>V. Nguyen</b>				DATE CONSIDERED <b>12/11/03</b>			
EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609; Draw line through citation if not in conformance not considered. Include copy of this form with next communication to the applicant.							

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<b>U.S. PATENT DOCUMENTS</b>							
EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
UN	5,280,443	01/1994	Hidaka et al.				
UN	5,315,542	05/1994	Melzner				
UN	5,396,450	03/1995	Takashima et al.				
UN	5,396,451	03/1995	Erma				
UN	5,499,205	03/1996	Ahn et al.				
UN	5,534,732	07/1996	DeBrosse et al.				
UN	5,581,126	12/1996	Moench				
UN	5,625,234	04/1997	Suzuki et al.				
UN	5,670,815	09/1997	Childs et al.				
UN	5,680,347	10/1997	Takeuchi et al.				
UN	5,815,428	09/1998	Tsuruda et al.				
UN	5,838,038	11/1998	Takashima et al.				
UN	5,864,181	01/1999	Keeth				
UN	5,877,976	03/1999	Lattimore et al.				
				<b>Viet Q. Nguyen</b> <b>Primary Examiner</b>			
<b>FOREIGN PATENT DOCUMENTS</b>							
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
						YES	NO
<b>OTHER DOCUMENTS</b> <small>(Including Author, Title, Date, Pertinent Pages, Etc.)</small>							
UN		Kimura et al., "A Block-Oriented RAM with Half-Sized DRAM Cell and Quasi-Folded Data-Line Architecture", November 11, 1991, IEEE, Journal of Solid-State Circuit, pps. 1511-1518.					
UN		Takashima et al., "Open/Folded Bit-Line Arrangement for Ultra High-Density DRAMs", (Abstract), pps. 89-90.					
UN		Hidaka et al., "A Divided/Shared Bit-Line Sensing Scheme for ULSI DRAM Cores", Journal of Solid-State Circuit, Vol. 26, No. 4, April 1991, pps. 473-477.					
EXAMINER			DATE CONSIDERED				
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